Approved for use through 11/30/2005. OMB 0651-0035

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	Application Num	ber	8/453,732	
POWER OF ATTORNEY	Filing Date		May 30, 1995	
odko l	First Named Inve	,,,,,	Ronald T. Fulks	
0/	Title		METHOD OF MANUFA MATRIX LCD USING F	
INDICATION FORM	Art Unit		2515	
	Examiner Name	Т	ON, MINH TOAN T	
	Attorney Docket	Number >	(RX940013	
Lhoroby consists				
I hereby appoint: Practitioners at Customer Number				
OR				
☐ Practitioner(s) named below:				1
Name		Registrat	ion Number	1
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as my/our attorney(s) or agent(s) to prosecute the Trademark Office connected therewith.	e application identified	above, and ic	transact an business	in the Faterit and
D				
Please recognize or change the correspondence	e address for the abov	ve-identilied ap	phication to.	
The above-mentioned Customer Number: OR	24498			
The address associated with Customer Nu				
OR		· · · · · · · · · · · · · · · · · · ·		
☐ Firm <i>or</i> Individual Name				
Address				
Address	· · · · · · · · · · · · · · · · · · ·			
City	State		ZIP	
Country				
Telephone				
I am the:	Fax			
	Fax			
Applicant/Inventor.	l			
☐ Applicant/Inventor. ☐ Assignee of record of the entire interest. S	Gee 37 CFR 3.71.			
 ☐ Applicant/Inventor. ☑ Assignee of record of the entire interest. S Certificate under 37 CFR 3.73(b) is enclose 	Gee 37 CFR 3.71.		cord	
☐ Applicant/Inventor. ☐ Assignee of record of the entire interest. S Certificate under 37 CFR 3.73(b) is enclose SIGNATUR	see 37 CFR 3.71. ed. (Form PTO/SB/96). RE of Applicant or As		cord	
☐ Applicant/Inventor. ☐ Assignee of record of the entire interest. S Certificate under 37 CFR 3.73(b) is enclose SIGNATUF Name Richard LaPeruta (Reg. No.	see 37 CFR 3.71. ed. (Form PTO/SB/96). RE of Applicant or As		cord	
☐ Applicant/Inventor. ☐ Assignee of record of the entire interest. S Certificate under 37 CFR 3.73(b) is enclose SIGNATUR	see 37 CFR 3.71. ed. (Form PTO/SB/96). RE of Applicant or As		Cord	

This collection of information is required by 37 CFR 1.31 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required.

Submit multiple forms if more than one signature is required, see below*.

*Total of 2 forms are submitted.

PTO/SB/96 (06-09)

Approved for use through 06/30/2009. OMB 0651-0031

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	18	£)	STATE	IENT U	INDER 37	CFR 3.73(b)			
Applicant/	/Patent Of	BADETHE MSON LI	CENSING		C	Sustomer No.	24498	Docket No:	XRX940013
		at No.: Appln. 08/45		5,621,5	56 File	d/Issue Date:	Filed May	30,1995; Iss	ued Apr.15,1997
Titled:	METHOD	OF MANUFACTU	RING ACTIV	E MAT	RIX LCD U	SING FIVE N	MASKS		
THOMSO	ON LICENS	SING		, a C	orporation				
(Name of As	ssignee)				(Type of Assign	ee, e.g., corporatio	n, partnership,	university, govern	ment agency, etc.
states tha	at it is:								
1.	the assigr	nee of the entire right	, title, and inte	erest in;					
2.		ee of less than the eint (by percentage) of				%); or			
3.	the assigr	nee of an undivided in	nterest in the	entirety o	of (a comple	te assignment	from one of	the joint inven	itors was made)
the paten	t application	/patent identified abo	ove, by virtue	of either	:				
A.	the United	ment from the invent d States Patent and Tefore is attached.							as recorded in , or for which a
в. X	A chain of	f title from the inventor	or(s), of the pa	itent apr	olication/pate	ent identified at	ove, to the	current assign	ee as follows:
-	1. From:	5 U - D.T. V	•		•			•	
		The document was related 007661		e United				at copy thereof is	attached.
	2. From:	Xerox Corp.				To: Bank C	ne, NA		
		The document was	recorded in th	e United	l States Pate	ent and Tradem	nark Office a	at	
		Reel 013153	, F	rame <u>00</u>	001	, or f	or which a	copy thereof is	attached.
	3. From:	Bank One, NA				To: Xerox (Corp.		
		The document was	recorded in th	e United	l States Pate	ent and Tradem	nark Office a	at	
		Reel 020571	, i	rame 0	851	, or f	or which a	copy thereof is	attached.
X	Additiona	al documents in the c	chain of title ar	e listed	on a suppler	mental sheet(s)).		
		/ 37 CFR 3.73(b)(1)(y is being, submitted					from the o	riginal owner to	o the assignee was,
		arate copy (<i>i.e.</i> , a tru ith 37 CFR Part 3, to							signment Division in
The unde	- ' . '	ose title is supplied b	pelow) is author	orized to	act on beha	of the assign	nee.		
	<u> </u>	Theut					Ju	ly 8, 2010	
	Signature							Date	
		uta (Reg. No. 5125	(2)				Pa —	tent Counsel	
Р	Printed or Tv	ped Name						Title	

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



SUPPLEMENTAL SHEET

STATEMENT UNDER 37 CFR 3.73(b)

Applican	pticant/Patent Owner: THOMSON LICENSING		Cu	Customer No. 24498 Docket No: XRX940013			XRX940013	
	pplication No./Patent No.: Appln. 08/453,732; Pat. 5,621,556							
Titled:	METHOD OF MA	ANUFACTURING	S ACTIVE MATRI	IX LCD US	ING FIVE M	IASKS		
THOMS	ON LICENSING		,a Cor	poration				<u> </u>
(Name of A	Assignee)		ന	pe of Assignee	e, e.g., corporation	, pairenh t	, university, govern	ment agency, etc.
4. F	rom: Xerox Cor	p.		To	JP Morga	n Chase	Bank	
	The documer	•	the United State Frame 0476					attached.
5. F	rom: Bank One	, NA		To	: Xerox Co	np		
	The documen	nt was recorded in	n the United State	s Patent an	nd Trademari	k Office at	t	
	Reel <u>02057</u>	<u>'1 </u>	Frame 0928		or for 1	which a c	opy thereof is	attached.
6. F	rom: Bank One	, NA	·	To	: Xerox Co	rp.		·
	The document Reel 02058		n the United State Frame 0202					attached.
7.	From: JP Morga	ın Chase Bank		То	Xerox Cor	р.		
			n the United State Frame 0463					ittached.
8.	From: JP Morga	n Chase Bank		То	Xerox Cor	p.		
			n the United State					
	Reel 0212	91	. Frame 0203		, orforw	vihiich a co	xpy thereof is a	ttachedi.
9.	From: Xerox Co	rp.; Palo Alto Ri	esearch Center I	nc. To	Thomson	Licensin	g LLC	
	The docume	ent was recorded i	n the United State	s Patent an	id Trademan	Office at		
	Reel 0225	75	. Frame <u>0781</u>	-	or for v	which a co	py thereof is a	itached.
10. Fr	rom: Thomson	Licensing LLC		То	: Thomson	Licensin	·g	
	The document		n the United State Frame 0746	s Palent ar			t opy thereof is	attached.



POWER OF ATTORNEY THOMSON LICENSING

We,

THOMSON LICENSING

46, Quai A. Le Gallo

F-92100 Boulogne-Billancourt

France

do hereby grant

Robert D. Shedd

Vice President, U.S. Patent Operations

Thomson Licensing LLC Two Independence Way Princeton, New Jersey 08540

a revocable, non-exclusive and delegable power of attorney to act for us (including the signing of requisite documents) in proceedings concerning patents and applications for patents, including international and other multi-country patents and applications for patents, in our name in the Patent Offices in all countries worldwide from January 1, 2009.

DATED this 13 day of January, in the year 2009.

Signature:

Typed Name As Signed:

Title:

Béatrix de Russé

Executive Vice-President

Licensing, Research & Innovation



POWER OF ATTORNEY THOMSON LICENSING

THOMSON LICENSING 46, Quai A. Le Gallo F-92100 Boulogne-Billancourt France

does hereby grant

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a revocable, non-exclusive and delegable power of attorney to act for us (including the signing of requisite documents) in proceedings concerning patents and applications for patents, including international and other multi-country patents and applications for patents, in our name in the Patent Offices in all countries worldwide from January 1, 2007.

SIGNED

Robert D. Shedd

Vice President, U.S. Patent Operations

Thomson Licensing LLC and

Attorney In Fact for

THOMSON LICENSING

WITNESS

Canda Fornacotto

PATENT ASSIGNMENT AGREEMENT

Between:

Thomson Licensing LLC, a Delaware, United States of America corporation having offices at Two Independence Way, Princeton, N.J. 08540, United States of America,

hereinafter referred to as the "Assignor".

and:

Thomson Licensing, a company organized and existing under the laws of France and having offices at 46, Quai Alphonse Le Gallo, 92100 Boulogne-Billancourt, France,

hereinafter referred to as the "Assignee".

WHEREAS Assignor is the owner or registered owner of certain patents and patent applications set forth on Exhibit 1 hereto (together with any and all related patents or patent applications that directly claim priority to the patents and patent applications set forth on Exhibit 1, including all corresponding patents and applications worldwide therefor and all patents (including utility models, and certificates of inventorship) resulting from reissues, continuations, continuations-in-part, divisions, renewals, reexaminations, substitutions and extensions of such patents or patent applications, all of the foregoing referred to as the "Purchased Patent Assets");

WHEREAS Assignor and Assignee have agreed, for good and valuable consideration, that all of Assignor's right, title and interest in and to the Purchased Patent Assets shall be assigned to Assignee;

THEREFORE, both parties hereby agree as follows:

ARTICLE 1

Assignor hereby assigns, conveys and transfers all right, title and interest in and to the Purchased Patent Assets

Assignee hereby accepts such assignment, conveyance and transfer to it of the foregoing Purchased Patent Assets.

In consequence, Assignee shall have all right, title and interest in and to the foregoing Purchased Patent Assets, including the right to exploit, assign, and license them.

ARTICLE 2

Assignee will have the right to institute, continue or defend, any suit or action dealing with the foregoing Purchased Patent Assets.

To such effect, Assignee is subrogated to all Assignor's rights and actions, in substitution for those of Assignor, both with respect to claims and defenses.

ARTICLE 3

The foregoing assignment is concluded for good and valuable consideration, the sufficiency of which is expressly acknowledged by the parties

ARTICLE 4

The present Patent Assignment Agreement may be registered by or for the Assignee, at its expense, before the appropriate Patent Office(s).

ARTICLE 5

Once executed by Assignor and Assignee, the present Patent Assignment Agreement shall come into effect, as between the parties, retroactively as of October 1, 2008.

IN WITNESS WHEREOF, each of the parties hereto has caused the present Patent Assignment Agreement to be executed in two (2) original copies, one (1) for each party, by its duly authorized officer or representative.

ASSIGNOR

By (title and signature): Shephon D San ve h

Pher dent Thomson Licensing LLC

ASSIGNEE

By (title and signature):

Deceder 31, 20.0

EXHIBIT 1

	PATENT NUMBER/		
	APPLICATION NUMBER	JURISDICTION	TITLE
1	5081513	US	ELECTRONIC DEVICE WITH RECOVERY LAYER PROXIMATE TO ACTIVE LAYER
2	5153420	US	TIMING INDEPENDENT PIXEL-SCALE LIGHT SENSING APPARATUS
3	5166960	US	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
•	• 3199899	JPN	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
•	• 0570115	EPC (GB, FR, DE)	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
4	5204661	US	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
•	• 3251964	JPN	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
•	• 0490683	EPC (GB, FR, DE)	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
5	5315418	US	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY WITH LIGHT COUPLING LENS ARRAY DISPOSED ALONG THE RED-GREEN LIGHT PATH
6	5366926	US	LOW TEMPERATURE PROCESS FOR LASER DEHYDROGENATION AND CRYSTALLIZATION OF AMORPHOUS SILICON
7	5401982	US	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS
•		CAN	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS
•	• 0670604	EPC (GB, FR, DE)	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS

			LEWIS OFF AVIO MEMBER
8	5442467	US	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS
1			EFFICIENCY OF A LIQUID CRYSTAL
İ			DISPLAY EMPLOYING FIBEROPTIC
		ł	FACEPLATE ELEMENTS
•	• 3578824	JPN	ENHANCED OFF-AXIS VIEWING
	0070024	3. 11	PERFORMANCE AND LUMINOUS
]	EFFICIENCY OF A LIQUID CRYSTAL
	j		DISPLAY EMPLOYING FIBEROPTIC
			FACEPLATE ELEMENTS
•	• 0674209	EPC (GB, FR, DE)	ENHANCED OFF-AXIS VIEWING
			PERFORMANCE AND LUMINOUS
		1	EFFICIENCY OF A LIQUID CRYSTAL
ĺ	1		DISPLAY EMPLOYING FIBEROPTIC
			FACEPLATE ELEMENTS
•	• 2138072	CAN	ENHANCED OFF-AXIS VIEWING
		Ī	PERFORMANCE AND LUMINOUS
	J		EFFICIENCY OF A LIQUID CRYSTAL
1			DISPLAY EMPLOYING FIBEROPTIC
			FACEPLATE ELEMENTS
9	5491347	US	THIN-FILM STRUCTURE WITH DENSE
			ARRAY OF BINARY CONTROL UNITS
	L====	110	FOR PRESENTING IMAGES FULL COLOR DISPLAY WITH GRADIENT
10	5504597	US	INDEX LENS ARRAY DISPOSED
			BETWEEN PHOSPHOR EMITTERS AND
1		· ·	LIQUID CRYSTAL DISPLAY
11	5504598	US	LARGE SCREEN FULL COLOR DISPLAY
''	0004000	100	WITH PLURAL ADJACENT DISPLAY
	į.	•	PANELS AND ENLARGING GRADED
			INDEX LENS ARRAY
12	5518805	US	HILLOCK-FREE MULTILAYER METAL
			LINES FOR HIGH PERFORMANCE THIN
			FILM STRUCTURES
•	• 7095231	JPN (Pending)	HILLOCK-FREE MULTILAYER METAL.
			LINES FOR HIGH PERFORMANCE THIN
			FILM STRUCTURES
•	• 0681328	EPC (GB, FR, DE)	HILLOCK-FREE MULTILAYER METAL
i			LINES FOR HIGH PERFORMANCE THIN
			FILM STRUCTURES
13	5528082	US	THIN-FILM STRUCTURE WITH TAPERED
		<u> </u>	FEATURE
14	5550656	US	FULL COLOR DISPLAY WITH PLURAL
İ			TWO-DIMIENSIONAL PLANAR ARRAYS
		110	OF LENSLETS
15	5557534	US	FORMING ARRAY WITH METAL SCAN
		ł	LINES TO CONTROL SEMICONDUCTOR
			GATE LINES FORMING ARRAY WITH METAL SCAN
•	• 0721215	EPC (GB, FR, DE)	FURING ARRAT WITH WETAL SCAN

			LINES TO CONTROL SEMICONDUCTOR
L			GATE LINES
16	5589847	US	SWITCHED CAPACITATOR ANALOG
			CIRCUITS USING POLYSILICON THIN
	j		FILM TECHNOLOGY
•	• 2049058	JPN	SWITCHED CAPACITATOR ANALOG
			CIRCUITS USING POLYSILICON THIN
			FILM TECHNOLOGY
•	• 0540163	EPC (GB, FR, DE)	SWITCHED CAPACITATOR ANALOG
			CIRCUITS USING POLYSILICON THIN
	İ		FILM TECHNOLOGY
17	5600155	US	ARRAY WITH METAL SCAN LINES
	1	1	CONTROLLING SEMICONDUCTOR GATE
1	1 .		LINES
•	• 0721213	EPC (GB, FR, DE)	ARRAY WITH METAL SCAN LINES
]			CONTROLLING SEMICONDUCTOR GATE
			LINES
18	5608245	US	ARRAY ON SUBSTRATE WITH REPAIR
			LINE CROSSING LINES IN THE ARRAY
•	• 3938959	JPN	ARRAY ON SUBSTRATE WITH REPAIR
1			LINE CROSSING LINES IN THE ARRAY
•	• 0780766	EPC (GB, FR, DE)	ARRAY ON SUBSTRATE WITH REPAIR
			LINE CROSSING LINES IN THE ARRAY
19	5608557	US	CIRCUITRY WITH GATE LINE CROSSING
l	1		SEMICONDUCTOR LINE AT TWO OR
			MORE CHANNELS
•	• 3952517	JPN (Divisional	CIRCUITRY WITH GATE LINE CROSSING
	• 2005-349299	Pending)	SEMICONDUCTOR LINE AT TWO OR
ł	(DIV)		MORE CHANNELS
•	• 0721214	EPC (GB, FR, DE)	CIRCUITRY WITH GATE LINE CROSSING
	1		SEMICONDUCTOR LINE AT TWO OR
			MORE CHANNELS
20	5821556	US	ACTIVE MATRIX LIQUID CRYSTAL
			DEVICE AND MANUFACTURING
			METHOD
•	• 0745886	EPC (GB, FR, DE)	ACTIVE MATRIX LIQUID CRYSTAL
		1	DEVICE AND MANUFACTURING
			METHOD
21	5642125	US	TWO PATH LIQUID CRYSTAL LIGHT
			VALVE COLOR DISPLAY
•	• 3329887	JPN	TWO PATH LIQUID CRYSTAL LIGHT
			VALVE COLOR DISPLAY
•	• 0579382	EPC (GB, FR, DE)	TWO PATH LIQUID CRYSTAL LIGHT
			VALVE COLOR DISPLAY

22	5648674	US	ARRAY CIRCUITRY WITH CONDUCTIVE
	İ		LINES, CONTACT LEADS, AND
			STORAGE CAPACITOR ELECTRODE ALL
1 .	1		FORMED IN LAYER THAT INCLUDES
			HIGHLY CONDUCTIVE METAL
23	5654970	US	ARRAY WITH REDUNDANT INTEGRATED
			SELF-TESTING SCAN DRIVERS
. •	• 3739874	JPN	ARRAY WITH REDUNDANT INTEGRATED
			SELF-TESTING SCAN DRIVERS
24	5682211	US	INTEGRATED DARK MATRIX FOR AN
1			ACTIVE MATRIX LIQUID CRYSTAL
ĺ			DISPLAY WITH PIXEL ELECTRODES
			OVERLAPPING GATE AND DATA LINES
•	• 8127583	JPN (Pending)	INTEGRATED DARK MATRIX FOR AN
	1		ACTIVE MATRIX LIQUID CRYSTAL
	1		DISPLAY WITH PIXEL ELECTRODES
	·		OVERLAPPING GATE AND DATA LINES
•	• 96303898 9	EPC (GB, FR, DE)	INTEGRATED DARK MATRIX FOR AN
		(Pending)	ACTIVE MATRIX LIQUID CRYSTAL
	1		DISPLAY WITH PIXEL ELECTRODES
			OVERLAPPING GATE AND DATA LINES
25	5693567	US	SEPARATELY ETCHING INSULATING
			LAYER FOR CONTACTS WITHIN ARRAY
			AND FOR PERIPHERAL PADS
26	5693983	US .	THIN-FILM STRUCTURE WITH
			CONDUCTIVE MOLYBDENUM-
<u> </u>			CHROMIUM LINE
•	• 0680088	EPC (GB, FR, DE)	THIN-FILM STRUCTURE WITH
			CONDUCTIVE MOLYBDENUM-
		1	CHROMIUM LINE
27	5694053	US	DISPLAY MATRIX TESTER
28	5703382	US	ARRAY HAVING MULTIPLE CHANNEL
	•		STRUCTURES WITH CONTINUOUSLY
		<u> </u>	DOPED INTERCHANNEL REGIONS
29	5703621	`US	UNIVERSAL DISPLAY THAT PRESENTS
]	ALL IMAGE TYPES WITH HIGH IMAGE
		<u> </u>	FIDELITY
30	5707744	US	SOLID-PHASE EPITAXIAL
ļ		1	CRYSTALLIZATION OF AMORPHOUS
			SILICON FILMS ON INSULATING
		<u> </u>	SUBSTRATES

			EDITAVIAL
•	• 8313160	JPN (Pending)	SOLID-PHASE EPITAXIAL
		1	CRYSTALLIZATION OF AMORPHOUS
ļ			SILICON FILMS ON INSULATING
L			SUBSTRATES
•	• 0782178	EPC (GB, FR, DE)	SOLID-PHASE EPITAXIAL
			CRYSTALLIZATION OF AMORPHOUS
			SILICON FILMS ON INSULATING
}	1		SUBSTRATES
31	5717223	US	ARRAY WITH AMORPHOUS SILICON
ĺ	ĺ		TFTS IN WHICH CHANNEL LEADS
		1	OVERLAP INSULATING REGION NO
1			MORE THAN MAXIMUM OVERLAP
•	• 8335053	JPN (Pending)	ARRAY WITH AMORPHOUS SILICON
1		, ,	TFTS IN WHICH CHANNEL LEADS
1			OVERLAP INSULATING REGION NO
Ì			MORE THAN MAXIMUM OVERLAP
•	• 0780909	EPC (GB, FR, DE)	ARRAY WITH AMORPHOUS SILICON
			TFTS IN WHICH CHANNEL LEADS
	1		OVERLAP INSULATING REGION NO
	1	1	MORE THAN MAXIMUM OVERLAP
32	5726730	US	OPTICAL EQUIVALENTS OF FIBER
		1	OPTIC FACE PLATES USING REACTIVE
		1	LIQUID CRYSTALS AND POLYMERS
33	5731803	US	ARRAY WITH LIGHT ACTIVE UNITS
		1	SIZED TO ELIMINATE ARTIFACT FROM
			SIZE DIFFERENCE
•	• 96309251.5	EPC (GC, FR, DE)	ARRAY WITH LIGHT ACTIVE UNITS
ļ		(Pending)	SIZED TO ELIMINATE ARTIFACT FROM
			SIZE DIFFERENCE
34	5733641	US	BUFFERED SUBSTRATE FOR
			SEMICONDUCTOR DEVICES
•	• 9148652	JPN (Pending)	BUFFERED SUBSTRATE FOR
			SEMICONDUCTOR DEVICES
35	5733804	US	FABRICATING FULLY SELF-ALIGNED
			AMORPHOUS SILICON DEVICE
•	 8335050 	JPN (Pending)	FABRICATING FULLY SELF-ALIGNED
			AMORPHOUS SILICON DEVICE
•	• 0780892	EPC (GB, FR, DE)	FABRICATING FULLY SELF-ALIGNED
•]		AMORPHOUS SILICON DEVICE
36	5744202	US	ENHANCEMENT OF HYDROGENATION
] -	OF MATERIALS ENCAPSULATED BY AN
			OXIDE
•	• 9266852	JPN (Pending)	ENHANCEMENT OF HYDROGENATION
•	- 020002		OF MATERIALS ENCAPSULATED BY AN
			OXIDE
•	• 97307393.5	EPC (GB, FR, DE)	ENHANCEMENT OF HYDROGENATION
•	9/30/383.3	(Pending)	OF MATERIALS ENCAPSULATED BY AN
		(1 GIMINS)	OXIDE
27	5751300	US	ENHANCED OFF-AXIS VIEWING
37	5751390	103	PITITULE OIT-FORD TIENINO

	.,		
			PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
•	• 9343367	JPN (Pending)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
•	• 97309846 0	EPC (GB, FR, DE) (Pending)	PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
38	5782665	US	FABRICATING ARRAY WITH STORAGE CAPACITOR BETWEEN CELL ELECTRODE AND DARK MATRIX
•	• 96309521.1	EPC (GB, FR, DE) (Pending)	FABRICATING ARRAY WITH STORAGE CAPACITOR BETWEEN CELL ELECTRODE AND DARK MATRIX
39	5831258	US	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 2204553	CAN	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 9217511	JPN (Pending)	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 97306165.8	EPC (GB, FR, DE) (Pending)	AMPLIFIER
40	5867240	US	LIQUID CRYSTAL CELL CONSTRUCTED TO PRODUCE A HIGHLY ANISOTROPIC LIGHT DISTRIBUTION POSSESSING EXTREMELY HIGH CONTRAST AROUND A NARROW MERIDIAN
41	5867242	US	ELECTRICALLY ISOLATED PIXEL ELEMENT IN A LOW VOLTAGE ACTIVATED ACTIVE MATRIX LIQUID CRYSTAL DISPLAY AND METHOD

•	• 0679922	EPC (GB, FR, DE)	ELECTRICALLY ISOLATED PIXEL ELEMENT IN A LOW VOLTAGE ACTIVATED ACTIVE MATRIX LIQUID CRYSTAL DISPLAY AND METHOD
42	5871826	US	PROXIMITY LASER DOPING TECHNIQUE FOR ELECTRONIC MATERIALS
•	• 9132630	JPN (Pending)	PROXIMITY LASER DOPING TECHNIQUE FOR ELECTRONIC MATERIALS
43	5875012	US	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
•	• 10016301	JPN (Pending)	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
•	• 0856768	EPC (GB, FR, DE)	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
44	5893949	บร	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES
45	5899711	US	METHOD FOR ENHANCING HYDROGENATION OF THIN FILM TRANSISTORS USING A METAL CAPPING LAYER AND METHOD FOR BATCH HYDROGENATION
46	5917464	US	COMBINATION OF 2-D DETECTOR ARRAY WITH DISPLAY FOR IMAGE PROCESSING
•	• 0708400	EPC (GB, FR, DE)	COMBINATION OF 2-D DETECTOR ARRAY WITH DISPLAY FOR IMAGE PROCESSING
47	5920401	US	COMPACT DOCUMENT IMAGER
•	• 6318590	JPN (Pending)	COMPACT DOCUMENT IMAGER
48	5928819	US	METHODS TO FABRICATE OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING REACTIVE LIQUID CRYSTALS AND POLYMERS
49	5956113	US	BISTABLE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
50	5959711	US	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE HAVING FIBER CLADDING MATERIAL

		3	
•	• 0747738	EPC (GB, FR, DE)	PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE HAVING FIBER CLADDING MATERIAL
51	5978063	us	SMART SPACERS FOR ACTIVE MATRIX LIQUID CRYSTAL PROJECTION LIGHT VALVES
•	• 10102810	JPN (Pending)	SMART SPACERS FOR ACTIVE MATRIX LIQUID CRYSTAL PROJECTION LIGHT VALVES
52	6019796	US	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
•	• 10298516	JPN (Pending)	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
•	• 0913860	EPC (GB, FR, DE)	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
53	6020223	US	METHOD OF MANUFACTURING A THIN FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE AND REDUCED FEED-THROUGH VOLTAGE
54	6034756	US	LCDS WITH WIDE VIEWING ANGLE
•	• 10128444	JPN (Pending)	LCDS WITH WIDE VIEWING ANGLE
55	6040812	US	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
•	• 9155118	JPN (Pending)	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
•	• 97304178 3	EPC (GB, FR, DE) (Pending)	ACTIVE MATRIX DISPLAY WITH INTEGRATED DRIVE CIRCUITRY
56	6078936	US	PRESENTING AN IMAGE ON A DISPLAY AS IT WOULD BE PRESENTED BY ANOTHER IMAGE OUTPUT DEVICE OR ON PRINTING CIRCUITRY
57	6107641	US	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE ANDREDUCED FEED-THROUGH VOLTAGE
•	• 10249510	JPN (Pending)	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE ANDREDUCED FEED-THROUGH VOLTAGE

•	• 0902481	EPC (GB, FR, DE)	THIN-FILM TRANSISTOR WITH REDUCED PARASITIC CAPACITANCE ANDREDUCED FEED-THROUGH VOLTAGE
58	6130732	US	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
•	• 10016302	JPN (Pending)	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
•	• 0856765	EPC (GB, FR, DE)	PAPER-WHITE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
59	6140668	US	SILICON STRUCTURES HAVING AN ABSORPTION LAYER
60	6160606	US	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
	• 10214520	JPN (Pending)	OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
	• 98306165.6	(Pending)	OPTIC FACE PLATES USING IRRADIATION SENSITIVE GLASS
61	6166800	US	SOLID-STATE IMAGE CAPTURE SYSTEM INCLUDING H-PDLC COLOR SEPARATION ELEMENT
•	• 11372106	JPN (Pending)	SOLID-STATE IMAGE CAPTURE SYSTEM INCLUDING H-PDLC COLOR SEPARATION ELEMENT
62	6245602	US	TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
•	• 2000-352356		TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
•	• 1102313	EPC (GB, FR, DE)	TOP GATE SELF-ALIGNED POLYSILICON TFT AND A METHOD FOR ITS PRODUCTION
63	6281891	US	DISPLAY WITH ARRAY AND MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG CONVERTER INTEGRATED CIRCUIT HAVING MANY OUTPUTS
•	• 3681470	JPN	DISPLAY WITH ARRAY AND MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG CONVERTER INTEGRATED CIRCUIT HAVING MANY OUTPUTS
•	• 207507	MX	DISPLAY WITH ARRAY AND MULTIPLEXER ON SUBSTRATE AND WITH ATTACHED DIGITAL-TO-ANALOG

		CONVERTER INTEGRATED CIRCUIT
		HAVING MANY OUTPUTS
6317189	US	HIGH-EFFICIENCY REFLECTIVE LIQUID
		CRYSTAL DISPLAY
• 11369152	JPN (Pending)	HIGH-EFFICIENCY REFLECTIVE LIQUID
		CRYSTAL DISPLAY
6339463	US	ENHANCED VIEWING ANGLE
		PERFORMANCE ON NON-POLARIZER
		BASED COLOR REFLECTIVE LIQUID
1	1	CRYSTAL DISPLAY USING A FIBER-
		OPTIC FACEPLATE
• PI9800969-9	BR	ENHANCED VIEWING ANGLE
	İ	PERFORMANCE ON NON-POLARIZER
		BASED COLOR REFLECTIVE LIQUID
		CRYSTAL DISPLAY USING A FIBER-
	110	OPTIC FACEPLATE METHODS OF ENCAPSULATING CORES
6406747	JUS	USING INK JETS OR FOGS
	<u> </u>	METHODS OF ENCAPSULATING CORES
• 4108965	JPN	USING INK JETS OR FOGS
04407754.0	EDC (CR ED DE)	METHODS OF ENCAPSULATING CORES
• 01127754.8		USING INK JETS OR FOGS
6456272		FLAP ARRAY UNDER FLUIDIC AND
0400273	03	ELECTRICAL CONTROL
6504175	IIS	HYBRID POLYCRYSTALLINE AND
0004175		AMORPHOUS SILICON STRUCTURES
		ON A SHARED SUBSTRATE
6628447	US	ARRAY OF ROTATABLE SOLID
10000		ELEMENTS FOR COLOR DISPLAY
6677926	us	ELECTROPHORETIC DISPLAY DEVICE
	• 11369152	• 11369152 JPN (Pending) 6339463 US • PI9800969-9 BR 6406747 US • 4108965 JPN • 01127754.8 EPC (GB, FR, DE) (Pending) 6456273 US 6504175 US

EXHIBIT 3 PATENT ASSIGNMENT

WHEREAS, Xerox Corporation, a New York corporation with offices at 45 Glover Ave., Norwalk, CT 06856, and including its wholly owned subsidiary, Palo Alto Research Center Incorporated, a Delaware corporation with offices at 3333 Coyote Hill Rd., Palo Alto, CA 94304 (collectively "Xerox") is the sole and exclusive owner of those certain patents and patent applications set forth on Attachment A hereto (together with any and all related patents or patent applications that directly claim priority to the patents and patent applications set forth on Attachment A, including all foreign corresponding patents and applications therefor (in all countries) and all patents (including utility models, and certificates of inventorship) resulting from reissues, continuations, continuations-in-part, divisions, renewals, reexaminations, substitutions and extensions of such patents or patent applications referred to as the "Patent Assets"); and

WHEREAS, Thomson Licensing LLC a limited liability company with offices at 2 Independence Way, Princeton, New Jersey 08540 ("TL LLC") desires to acquire all right, title and interest in, to and under the said Patents;

NOW, THEREFORE, for good and valuable consideration the sufficiency of which is acknowledged by the parties:

Xerox does hereby irrevocably and perpetually assign, convey, and transfer to TL LLC, all of Xerox's right, title and interest throughout the world, in and to the Patent Assets, all of which are to be held and enjoyed by Purchaser for its own use and enjoyment, and for the use and enjoyment of its successors, assigns or other legal representatives, to the end of the term or terms for which said Patent Assets are or may be granted, reissued or extended as fully and entirely as the same would have been held and enjoyed by Xerox, if this assignment and sale had not been made; together with all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patent Assets, including, without limitation, all causes of action and other enforcement rights for (i) damages, (ii) injunctive relief, and (iii) any other remedies of any kind for past, current and future infringement, and all rights to collect royalties or other payments under or on account of any of the Patents, all for TL LLC's own use and behalf, and for the use and behalf of its successors, assigns or other legal representatives.

Xerox hereby authorizes and requests the Commissioner of Patents and Trademarks, or an equivalent officer in any jurisdiction in which a Patent may have issued, to issue any and all Letters Patent on said inventions to Purchaser as assignee of the entire interest, and hereby covenants that Xerox has full right to convey the entire interest herein assigned, and that, except as otherwise explicitly agreed and acknowledged in writing between the parties, Xerox has not executed, and will not execute, any agreements in conflict therewith.

Signature Page Follows

XEROX CORPORATION

BY: SERVE VAWDED ROCK

Title: Xerox CTO

Signature:

Date: 8/4/2008

THOMSON LICENSING LLC

BY: DAVID T. SHONEMAN

Title: V €

Signature:

Date: 5 AUG ZOUS

PALO ALTO RESEARCH CENTER INCORPORATED

BY: DAMON C. MATTED

Title: VP

Signature:

Date:

4/08

Signature Page to Patent Assignment from Xerox Corporation and its wholly owned subsidiary Palo Alto Research Center Incorporated

To Thomson Licensing LLC

ATTACHMENT A

TO EXHIBIT 3 PATENT ASSIGNMENT

			PATENT ASSIGNMENT
	PATENT NUMBER/ APPLICATION NUMBER	JURISDICTION	TITLE
1	5081513	US	ELECTRONIC DEVICE WITH RECOVERY LAYER PROXIMATE TO ACTIVE LAYER
2	5153420	US	TIMING INDEPENDENT PIXEL-SCALE LIGHT SENSING APPARATUS
3	5166960	US	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
•	• 3199899	JPN	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
•	• 0570115	EPC (GB, FR, DE)	PARALLEL MULTI-PHASED A-SI SHIFT REGISTER FOR FAST ADDRESSING OF AN A-SI ARRAY
4	5204661	US	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
•	• 3251964	JPN	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
•	• 0490683	EPC (GB, FR, DE)	INPUT/OUTPUT PIXEL CIRCUIT AND ARRAY OF SUCH CIRCUITS
5	5315418	US	TWO PATH LIQUID CRYSTAL LIGHT VALVE COLOR DISPLAY WITH LIGHT COUPLING LENS ARRAY DISPOSED ALONG THE RED-GREEN LIGHT PATH
6	5366926	us	LOW TEMPERATURE PROCESS FOR LASER DEHYDROGENATION AND CRYSTALLIZATION OF AMORPHOUS SILICON
7	5401982	US	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS
•	• 2140403	CAN	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS
•	• 0670604	EPC (GB, FR, DE)	REDUCING LEAKAGE CURRENT IN A THIN-FILM TRANSISTOR WITH CHARGE CARRIER DENSITIES THAT VARY IN TWO DIMENSIONS

8	5442467	US	ENHANCED OFF-AXIS VIEWING PERFORMANCE AND LUMINOUS
1			
			EFFICIENCY OF A LIQUID CRYSTAL
ł			DISPLAY EMPLOYING FIBEROPTIC
	0570004	IDM	FACEPLATE ELEMENTS
•	• 3578824	JPN	ENHANCED OFF-AXIS VIEWING
			PERFORMANCE AND LUMINOUS
			EFFICIENCY OF A LIQUID CRYSTAL
			DISPLAY EMPLOYING FIBEROPTIC
			FACEPLATE ELEMENTS
•	• 0674209	EPC (GB, FR,	ENHANCED OFF-AXIS VIEWING
1		DE)	PERFORMANCE AND LUMINOUS
			EFFICIENCY OF A LIQUID CRYSTAL
		ľ	DISPLAY EMPLOYING FIBEROPTIC
			FACEPLATE ELEMENTS
•	• 2138072	CAN	ENHANCED OFF-AXIS VIEWING
		ŀ	PERFORMANCE AND LUMINOUS
}			EFFICIENCY OF A LIQUID CRYSTAL
		İ	DISPLAY EMPLOYING FIBEROPTIC
-		İ	FACEPLATE ELEMENTS
9	5491347	US	THIN-FILM STRUCTURE WITH DENSE
ļ			ARRAY OF BINARY CONTROL UNITS
			FOR PRESENTING IMAGES
10	5504597	US	FULL COLOR DISPLAY WITH
			GRADIENT INDEX LENS ARRAY
			DISPOSED BETWEEN PHOSPHOR
			EMITTERS AND LIQUID CRYSTAL
			DISPLAY
11	5504598	US	LARGE SCREEN FULL COLOR
<u> </u>			DISPLAY WITH PLURAL ADJACENT
			DISPLAY PANELS AND ENLARGING
			GRADED INDEX LENS ARRAY
12	5518805	US	HILLOCK-FREE MULTILAYER METAL
			LINES FOR HIGH PERFORMANCE
ł			THIN FILM STRUCTURES
•	• 7095231	JPN (Pending)	HILLOCK-FREE MULTILAYER METAL
1	, 300201	3 (. 5.15.119)	LINES FOR HIGH PERFORMANCE
1			THIN FILM STRUCTURES
•	• 0681328	EPC (GB, FR,	HILLOCK-FREE MULTILAYER METAL
	3 330 1020	DE)	LINES FOR HIGH PERFORMANCE
),	THIN FILM STRUCTURES
13	5528082	US	THIN-FILM STRUCTURE WITH
,0	332000E	1 30	TAPERED FEATURE
14	5550656	US	
'-	3330030	103	FULL COLOR DISPLAY WITH PLURAL
			TWO-DIMIENSIONAL PLANAR
15	EEE7E04	110	ARRAYS OF LENSLETS
15	5557534	US	FORMING ARRAY WITH METAL SCAN
İ			LINES TO CONTROL
		500 (05 ==	SEMICONDUCTOR GATE LINES
•	• 0721215	EPC (GB, FR,	FORMING ARRAY WITH METAL SCAN

		I DE\	THE TO CONTROL
		DE)	LINES TO CONTROL
10	55000.47	110	SEMICONDUCTOR GATE LINES
16	5589847	US	SWITCHED CAPACITATOR ANALOG
1			CIRCUITS USING POLYSILICON THIN
 	0040050	IDNI	FILM TECHNOLOGY
•	• 2049058	JPN	SWITCHED CAPACITATOR ANALOG
			CIRCUITS USING POLYSILICON THIN
	0540460	EDC (CR ED	FILM TECHNOLOGY
•	• 0540163	EPC (GB, FR, DE)	SWITCHED CAPACITATOR ANALOG CIRCUITS USING POLYSILICON THIN
		ושב)	FILM TECHNOLOGY
17	5600155	US	ARRAY WITH METAL SCAN LINES
1 1/	3000133	103	CONTROLLING SEMICONDUCTOR
			GATE LINES
•	• 0721213	EPC (GB, FR,	ARRAY WITH METAL SCAN LINES
•	0/21213	DE)	CONTROLLING SEMICONDUCTOR
) JC)	GATE LINES
18	5608245	US	ARRAY ON SUBSTRATE WITH REPAIR
.5	33002.0		LINE CROSSING LINES IN THE ARRAY
•	• 3938959	JPN	ARRAY ON SUBSTRATE WITH REPAIR
	,		LINE CROSSING LINES IN THE ARRAY
•	• 0780766	EPC (GB, FR,	ARRAY ON SUBSTRATE WITH REPAIR
		DE)	LINE CROSSING LINES IN THE ARRAY
19	5608557	US	CIRCUITRY WITH GATE LINE
			CROSSING SEMICONDUCTOR LINE
			AT TWO OR MORE CHANNELS
•	• 3952517	JPN (Divisional	CIRCUITRY WITH GATE LINE
	• 2005-349299	Pending)	CROSSING SEMICONDUCTOR LINE
	(DIV)		AT TWO OR MORE CHANNELS
•	• 0721214	EPC (GB, FR,	CIRCUITRY WITH GATE LINE
		DE)	CROSSING SEMICONDUCTOR LINE
			AT TWO OR MORE CHANNELS
20	5621556	US	ACTIVE MATRIX LIQUID CRYSTAL
]			DEVICE AND MANUFACTURING
			METHOD
•	0745886	EPC (GB, FR,	ACTIVE MATRIX LIQUID CRYSTAL
		DE)	DEVICE AND MANUFACTURING
			METHOD
21	5642125	US	TWO PATH LIQUID CRYSTAL LIGHT
		151	VALVE COLOR DISPLAY
•	• 3329887	JPN	TWO PATH LIQUID CRYSTAL LIGHT
		EDO (OE ES	VALVE COLOR DISPLAY
•	• 0579382	EPC (GB, FR,	TWO PATH LIQUID CRYSTAL LIGHT
		DE)	VALVE COLOR DISPLAY

		T. 12	
22	5648674	US	ARRAY CIRCUITRY WITH
:			CONDUCTIVE LINES, CONTACT
			LEADS, AND STORAGE CAPACITOR
	1		ELECTRODE ALL FORMED IN LAYER
			THAT INCLUDES HIGHLY
	·		CONDUCTIVE METAL
23	5654970	US	
23	3634970	03	ARRAY WITH REDUNDANT
1			INTEGRATED SELF-TESTING SCAN
			DRIVERS
•	• 3739874	JPN ·	ARRAY WITH REDUNDANT
			INTEGRATED SELF-TESTING SCAN
	ļ	8	DRIVERS
24	5682211	US	INTEGRATED DARK MATRIX FOR AN
			ACTIVE MATRIX LIQUID CRYSTAL
			DISPLAY WITH PIXEL ELECTRODES
			OVERLAPPING GATE AND DATA
	0407500	IDNI (Deservices)	LINES
•	• 8127583	JPN (Pending)	INTEGRATED DARK MATRIX FOR AN
			ACTIVE MATRIX LIQUID CRYSTAL
	•		DISPLAY WITH PIXEL ELECTRODES
	,		OVERLAPPING GATE AND DATA
			LINES
•	• 96303898.9	EPC (GB, FR,	INTEGRATED DARK MATRIX FOR AN
1		DE) (Pending)	ACTIVE MATRIX LIQUID CRYSTAL
		'`	DISPLAY WITH PIXEL ELECTRODES
			OVERLAPPING GATE AND DATA
			LINES
25	5693567	US	SEPARATELY ETCHING INSULATING
	3030307	100	LAYER FOR CONTACTS WITHIN
	500000	110	ARRAY AND FOR PERIPHERAL PADS
26	5693983	US	THIN-FILM STRUCTURE WITH
			CONDUCTIVE
			MOLYBDENUM-CHROMIUM LINE
•	• 0680088	EPC (GB, FR,	THIN-FILM STRUCTURE WITH
		DE)	CONDUCTIVE
		·	MOLYBDENUM-CHROMIUM LINE
27	5694053	US	DISPLAY MATRIX TESTER
28	5703382	US	ARRAY HAVING MULTIPLE CHANNEL
		· = -	STRUCTURES WITH CONTINUOUSLY
			DOPED INTERCHANNEL REGIONS
29	5703621	US	
23	3703021	03	UNIVERSAL DISPLAY THAT
			PRESENTS ALL IMAGE TYPES WITH
		· · · · · · · · · · · · · · · · · · ·	HIGH IMAGE FIDELITY
30	5707744	US	SOLID-PHASE EPITAXIAL
.	-		CRYSTALLIZATION OF AMORPHOUS
			SILICON FILMS ON INSULATING
.			SUBSTRATES
			1

r		T	
•	• 8313160	JPN (Pending)	SOLID-PHASE EPITAXIAL
İ			CRYSTALLIZATION OF AMORPHOUS
			SILICON FILMS ON INSULATING
			SUBSTRATES
•	• 0782178	EPC (GB, FR,	SOLID-PHASE EPITAXIAL
1		DE)	CRYSTALLIZATION OF AMORPHOUS
		·	SILICON FILMS ON INSULATING
ŀ			SUBSTRATES
31	5717223	US	ARRAY WITH AMORPHOUS SILICON
			TFTS IN WHICH CHANNEL LEADS
			OVERLAP INSULATING REGION NO
			MORE THAN MAXIMUM OVERLAP
•	• 8335053	JPN (Pending)	ARRAY WITH AMORPHOUS SILICON
		` "	TFTS IN WHICH CHANNEL LEADS
			OVERLAP INSULATING REGION NO
			MORE THAN MAXIMUM OVERLAP
•	• 0780909	EPC (GB, FR,	ARRAY WITH AMORPHOUS SILICON
		DE)	TFTS IN WHICH CHANNEL LEADS
		1	OVERLAP INSULATING REGION NO
		1	MORE THAN MAXIMUM OVERLAP
32	5726730	US	OPTICAL EQUIVALENTS OF FIBER
			OPTIC FACE PLATES USING
			REACTIVE LIQUID CRYSTALS AND
			POLYMERS
33	5731803	US	ARRAY WITH LIGHT ACTIVE UNITS
			SIZED TO ELIMINATE ARTIFACT
			FROM SIZE DIFFERENCE
•	• 96309251.5	EPC (GC, FR,	ARRAY WITH LIGHT ACTIVE UNITS
		DE) (Pending)	SIZED TO ELIMINATE ARTIFACT
			FROM SIZE DIFFERENCE
34	5733641	US	BUFFERED SUBSTRATE FOR
			SEMICONDUCTOR DEVICES
•	• 9148652	JPN (Pending)	BUFFERED SUBSTRATE FOR
			SEMICONDUCTOR DEVICES
35	5733804	US	FABRICATING FULLY SELF-ALIGNED
			AMORPHOUS SILICON DEVICE
•	• 8335050	JPN (Pending)	FABRICATING FULLY SELF-ALIGNED
			AMORPHOUS SILICON DEVICE
•	• 0780892	EPC (GB, FR,	FABRICATING FULLY SELF-ALIGNED
		DE)	AMORPHOUS SILICON DEVICE
36	5744202	US	ENHANCEMENT OF
į			HYDROGENATION OF MATERIALS
			ENCAPSULATED BY AN OXIDE
•	• 9266852	JPN (Pending)	ENHANCEMENT OF
			HYDROGENATION OF MATERIALS
	• -		ENCAPSULATED BY AN OXIDE
•	• 97307393.5	EPC (GB, FR,	ENHANCEMENT OF
		DE) (Pending)	HYDROGENATION OF MATERIALS
		<u> </u>	ENCAPSULATED BY AN OXIDE
37	5751390	US	ENHANCED OFF-AXIS VIEWING

			
			PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
•	• 9343367	JPN (Pending)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
•	• 97309846.0	EPC (GB, FR, DE) (Pending)	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE IN CONJUNCTION WITH DUAL NEGATIVE RETARDERS AND A BRIGHTNESS ENHANCING FILM ON THE ILLUMINATION SOURCE
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39	5831258	US	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 2204553	CAN	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 9217511	JPN (Pending)	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
•	• 97306165.8	EPC (GB, FR, DE) (Pending)	PIXEL CIRCUIT WITH INTEGRATED AMPLIFIER
40	5867240	US	LIQUID CRYSTAL CELL CONSTRUCTED TO PRODUCE A HIGHLY ANISOTROPIC LIGHT DISTRIBUTION POSSESSING EXTREMELY HIGH CONTRAST AROUND A NARROW MERIDIAN
41	5867242	US	ELECTRICALLY ISOLATED PIXEL ELEMENT IN A LOW VOLTAGE ACTIVATED ACTIVE MATRIX LIQUID CRYSTAL DISPLAY AND METHOD

•	• 0679922	EPC (GB, FR, DE)	ELECTRICALLY ISOLATED PIXEL ELEMENT IN A LOW VOLTAGE ACTIVATED ACTIVE MATRIX LIQUID
42	5871826	US	PROXIMITY LASER DOPING TECHNIQUE FOR ELECTRONIC MATERIALS
•	• 9132630	JPN (Pending)	PROXIMITY LASER DOPING TECHNIQUE FOR ELECTRONIC MATERIALS
43	5875012	US	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
•	• 10016301	JPN (Pending)	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
•	• 0856768	EPC (GB, FR, DE)	BROADBAND REFLECTIVE DISPLAY, AND METHODS OF FORMING THE SAME
44	5893949	US	SOLID-PHASE EPITAXIAL CRYSTALLIZATION OF AMORPHOUS SILICON FILMS ON INSULATING SUBSTRATES
45	5899711	US	METHOD FOR ENHANCING HYDROGENATION OF THIN FILM TRANSISTORS USING A METAL CAPPING LAYER AND METHOD FOR BATCH HYDROGENATION
46	5917464	US	COMBINATION OF 2-D DETECTOR ARRAY WITH DISPLAY FOR IMAGE PROCESSING
•	• 0708400	EPC (GB, FR, DE)	COMBINATION OF 2-D DETECTOR ARRAY WITH DISPLAY FOR IMAGE PROCESSING
47	5920401	US	COMPACT DOCUMENT IMAGER
•	• 6318590	JPN (Pending)	COMPACT DOCUMENT IMAGER
48	5928819	US	METHODS TO FABRICATE OPTICAL EQUIVALENTS OF FIBER OPTIC FACE PLATES USING REACTIVE LIQUID CRYSTALS AND POLYMERS
49	5956113	US	BISTABLE REFLECTIVE DISPLAY AND METHODS OF FORMING THE SAME
50	5959711	US	ENHANCED OFF-AXIS VIEWING PERFORMANCE OF LIQUID CRYSTAL DISPLAY EMPLOYING A FIBEROPTIC FACEPLATE HAVING FIBER CLADDING MATERIAL

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•	• 0747738	EPC (GB, FR,	ENHANCED OFF-AXIS VIEWING
		DE)	PERFORMANCE OF LIQUID CRYSTAL
			DISPLAY EMPLOYING A FIBEROPTIC
			FACEPLATE HAVING FIBER CLADDING
51	5978063	US	MATERIAL
31	59/8063	05	SMART SPACERS FOR ACTIVE MATRIX
			LIQUID CRYSTAL PROJECTION LIGHT
	• 10102810	JPN (Pending)	VALVES
•	• 10102810	JEN (Fending)	SMART SPACERS FOR ACTIVE MATRIX LIQUID CRYSTAL PROJECTION LIGHT
			VALVES
52	6019796	US	METHOD OF MANUFACTURING A THIN
52		00	FILM TRANSISTOR WITH REDUCED
			PARASITIC CAPACITANCE AND
1			REDUCED FEED-THROUGH VOLTAGE
•	• 10298516	JPN (Pending)	METHOD OF MANUFACTURING A THIN
	10200010	07 14 (1 01 a.m.g)	FILM TRANSISTOR WITH REDUCED
			PARASITIC CAPACITANCE AND
			REDUCED FEED-THROUGH VOLTAGE
•	• 0913860	EPC (GB, FR,	METHOD OF MANUFACTURING A THIN
		DE)	FILM TRANSISTOR WITH REDUCED
		'	PARASITIC CAPACITANCE AND
			REDUCED FEED-THROUGH VOLTAGE
53	6020223	US	METHOD OF MANUFACTURING A THIN
			FILM TRANSISTOR WITH REDUCED
			PARASITIC CAPACITANCE AND
			REDUCED FEED-THROUGH VOLTAGE
54	6034756	US	LCDS WITH WIDE VIEWING ANGLE
	• 10128444	JPN (Pending)	LCDS WITH WIDE VIEWING ANGLE
55	6040812	US	ACTIVE MATRIX DISPLAY WITH
	0.455440	IDM (Daniella)	INTEGRATED DRIVE CIRCUITRY
•	• 9155118	JPN (Pending)	ACTIVE MATRIX DISPLAY WITH
	07004470.0	FDC (OD FD	INTEGRATED DRIVE CIRCUITRY
•	• 97304178.3	EPC (GB, FR, DE) (Pending)	ACTIVE MATRIX DISPLAY WITH
56	6078936	US (Pending)	INTEGRATED DRIVE CIRCUITRY PRESENTING AN IMAGE ON A DISPLAY
30	0070900	03	AS IT WOULD BE PRESENTED BY
i i			ANOTHER IMAGE OUTPUT DEVICE OR
			ON PRINTING CIRCUITRY
57	6107641	US	THIN-FILM TRANSISTOR WITH
"	V.01071		REDUCED PARASITIC CAPACITANCE
			ANDREDUCED FEED-THROUGH
			VOLTAGE
•	• 10249510	JPN (Pending)	THIN-FILM TRANSISTOR WITH
·	- 10210010	· · (• • · · · · · · · · · · · · · · ·	REDUCED PARASITIC CAPACITANCE
	•		ANDREDUCED FEED-THROUGH
			VOLTAGE
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